

## INITIAL INFORMATION DATA SHEET

### APPLICATION INFORMATION

Filing Date:: 02-26-2004  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: Quintuple Modular Redundancy for  
High Reliability Circuits  
Implemented in Programmable Logic  
Devices  
Docket Number:: X-1102-1P-1D US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: Four  
Small Entity?:: No  
Petition included?:: No

### INVENTOR INFORMATION

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: US  
Status:: Full Capacity  
Given Name:: Stephen  
Middle Name:: M.  
Family Name:: Trimberger  
Street:: 1261 Chateau Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95120

### CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

**REPRESENTATIVE INFORMATION**

Representative Customer Number::	24309	
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**DOMESTIC PRIORITY INFORMATION**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	10/245,741	09-16-02
10/245,741	CIP	10/150,044	05-16-02

**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124